## 14-slot PXIe chassis CH-14 PXIe





New CH-14 PXIe chassis allows you to create high-performance multifunctional systems for collecting and processing large flows of information, as well as systems for monitoring and adjusting radio electronic equipment.

The chassis allows you to install up to 14 PXIe instrument modules. On the basis of Chassis CH-14 PXIe-PC, it is possible to create high-performance multifunctional systems for collecting and processing large flows of information, as well as systems for monitoring and adjusting radio-electronic equipment.

## Key features

- PCIe Gen3 support, UpLink and DownLink ports (8 PCIe Ianes) for connecting external equipment (PC, PXI chassis, AXIe chassis) via PCIe x8 cable interface
- Built-in PXI chassis system module

- The presence of a built-in matrix switcher of trigger events with the ability to relay trigger events signals to / from external equipment. Management, registration, formation of trigger events
- Selectable frequency reference source (internal / external) to ensure synchronous operation of peripheral modules in different chassis
- Ensures only PXIe peripheral modules function (no hybrid or PXI-1 slots)
- "Transparency" for PXIe drivers of peripheral modules (additional software is not required for operation, the modules are displayed in the PC Device Manager)
- Availability of a Web interface for configuring the system, viewing the status of PXI modules

## **Specifications**

Chassis type - PXIe	The number of peripheral slots - 14
PC connection (UpLink port) - PCIe x8 cable standard	PXI or AXIe Optional Chassis Connections (DownLink Port) - PCIe x8 Cable Standard
The exchange of information streams is carried out via the PCle Gen3 interface	Maximum system throughput - 128 Gb / s bidirectional streams (64 Gb / s one way, 8 lanes at 8 Gb / s)
Maximum Bandwidth Per Module - 64 Gb / s bidirectional streams (32 Gb / s one way, 4 lanes at 8 Gb / s)	Integrated controller that implements the function of the system module PXI chassis (does not take up additional space for installing peripheral modules)
<ul> <li>The integrated built-in PXIe modules synchronization system provides the following functions:</li> <li>Patching any trigger event on any line to any other line inside the chassis and to an external chassis connector</li> <li>Patching a trigger event from the external chassis connector to any trigger event lines inside the chassis</li> <li>Possibility to invert (reverse polarity) signals on any chassis trigger lines</li> <li>Possibility to register and display the presence of trigger events on each chassis trigger event line</li> <li>Ability to generate trigger events on any chassis trigger event lines and external connector</li> <li>Selecting the source of the reference frequencies for the operation of the PXIe modules - internal frequency of the chassis or external frequency from the connector on the chassis</li> <li>Formation of the chassis reference frequency to the external chassis connector to create multiple crate systems operating in a single time frequency grid</li> <li>Control, switching, registration of trigger events, as well as selection of the reference frequency source is carried out via the chassis WEB interface. If necessary, the configuration can be saved to the chassis FLASH memory to retain the configuration of the trigger events and the frequency reference when the chassis is powered down.</li> </ul>	<ul> <li>Monitoring the status of each PXIe module in terms of the presence of problems with their power supply and functioning (monitoring the presence of PWRGD, Alarm, Alert signals from modules)</li> <li>Ability to power on / off each PXIe module if the modules support the PXI PWREN signal</li> <li>Trigger Matrix Switcher Control</li> <li>Selecting the Chassis Reference Source</li> <li>Write configuration to chassis FLASH</li> </ul>

